

FIG. 2

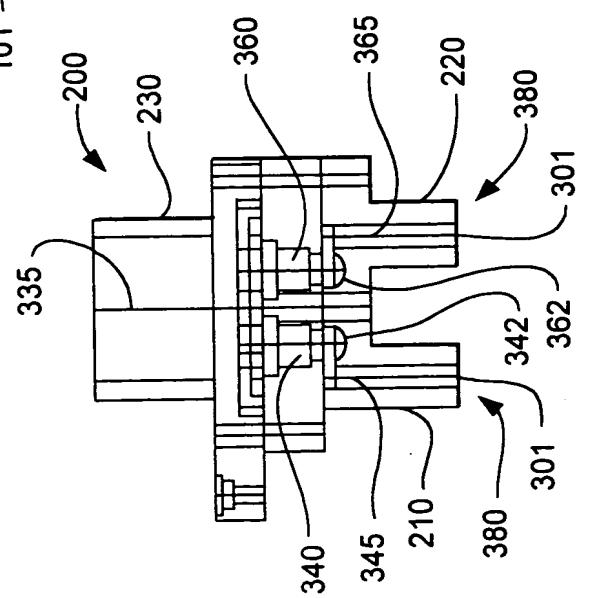
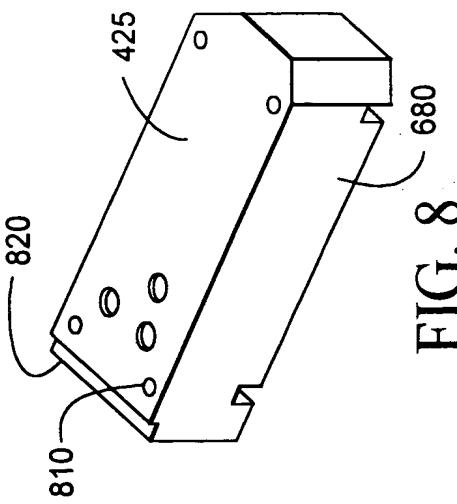
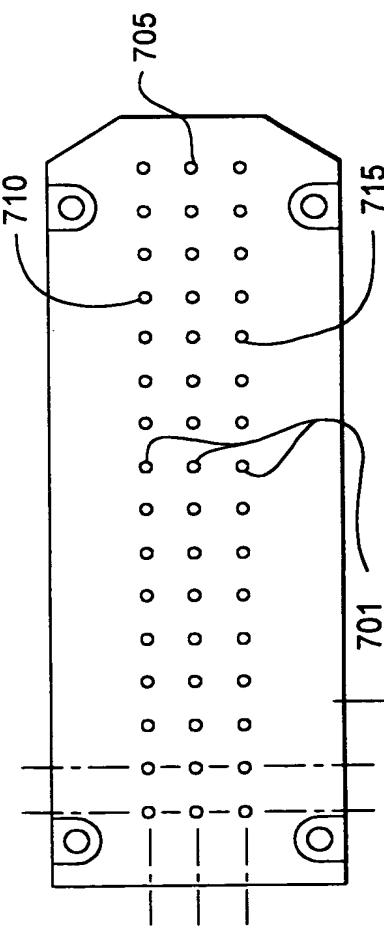
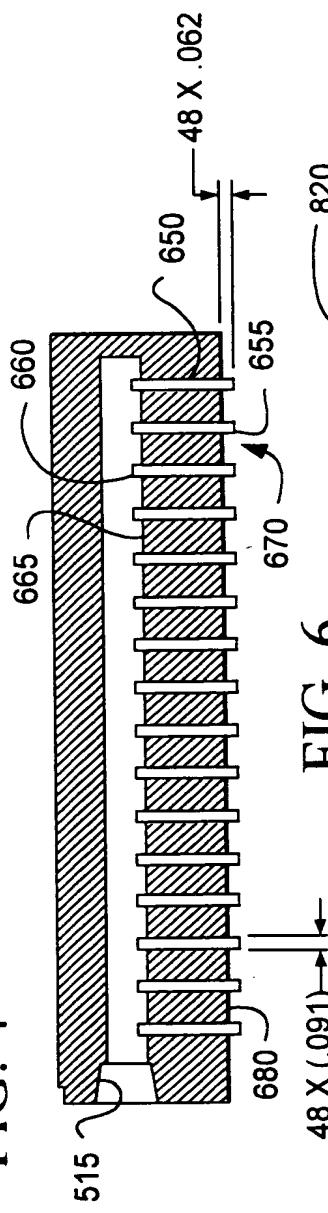
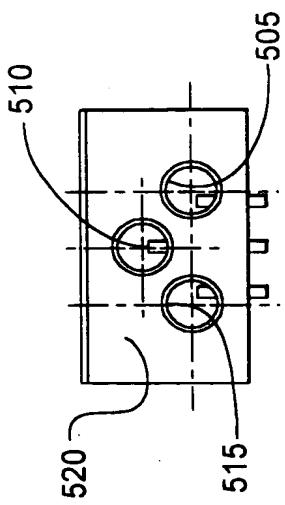
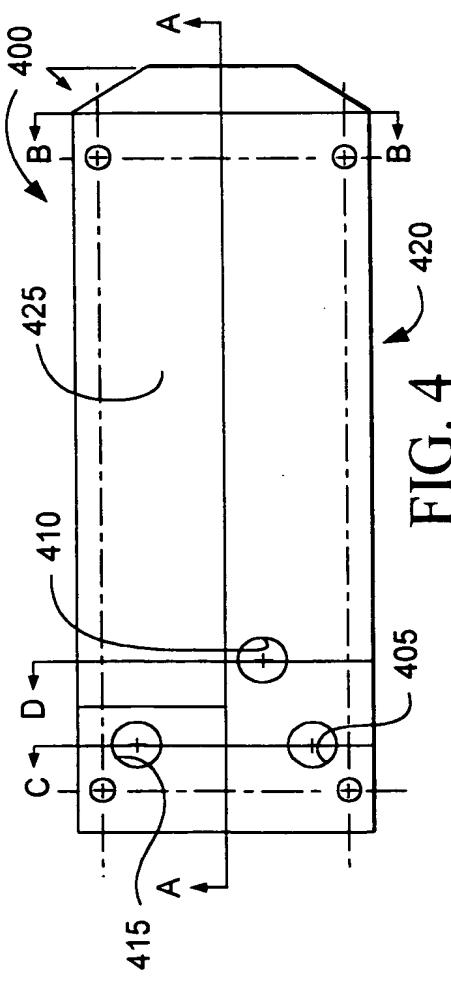


FIG. 3



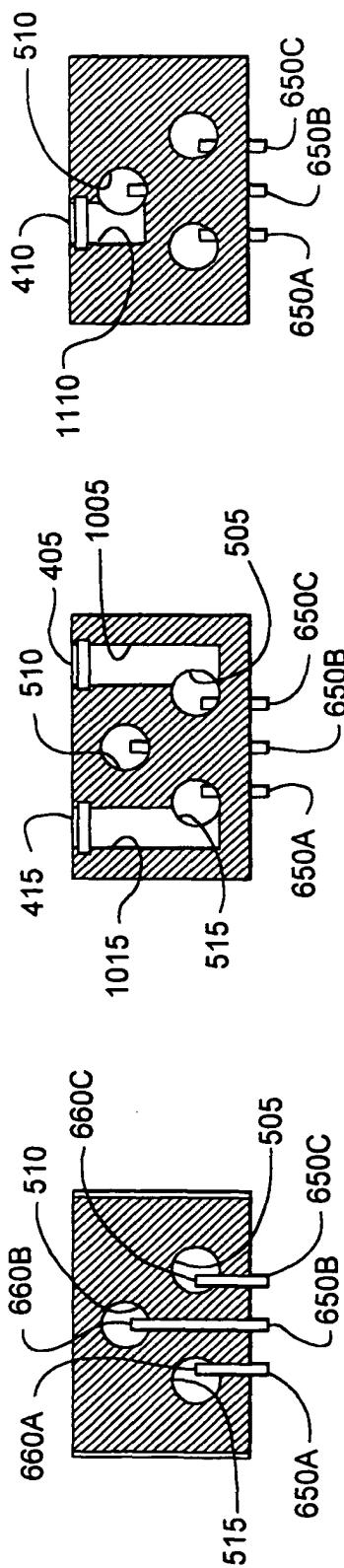
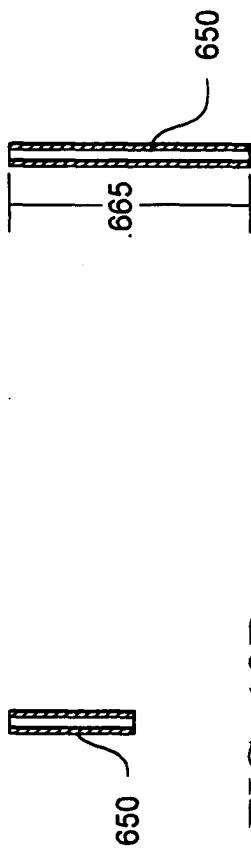


FIG. 10
FIG. 11



FIG. 13A
FIG. 13B



4/10

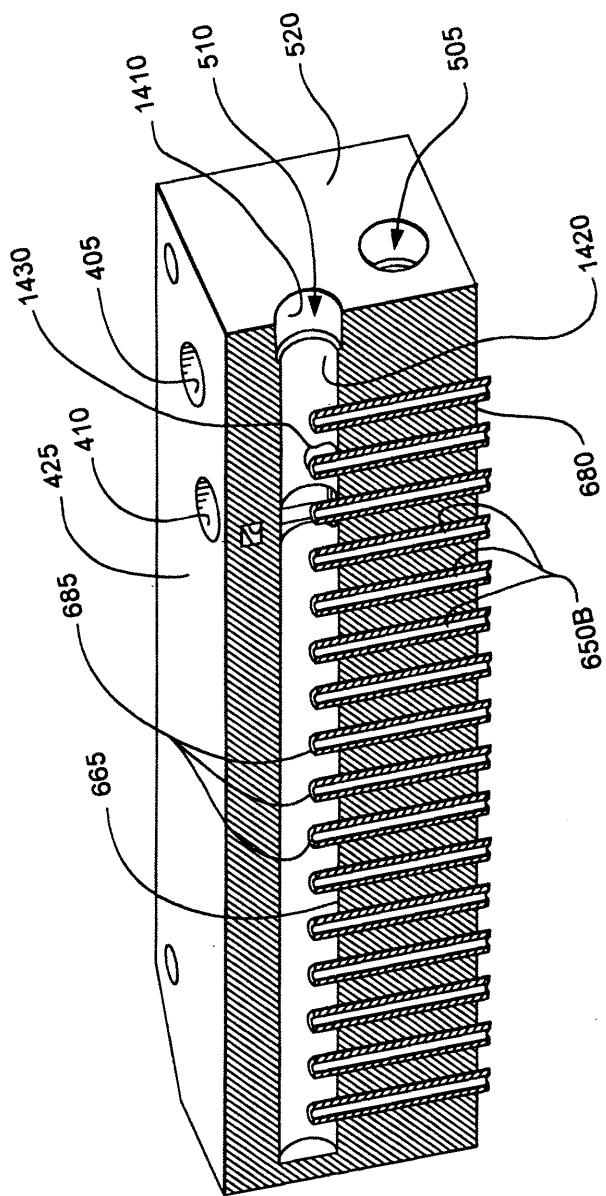


FIG. 14

DEVELOP RATES AT 121 WAFER LOCATIONS, DEVELOP ARM OFFSET=0mm
TOK 9; DOSE=12; DEVELOP TIME=10SEC.

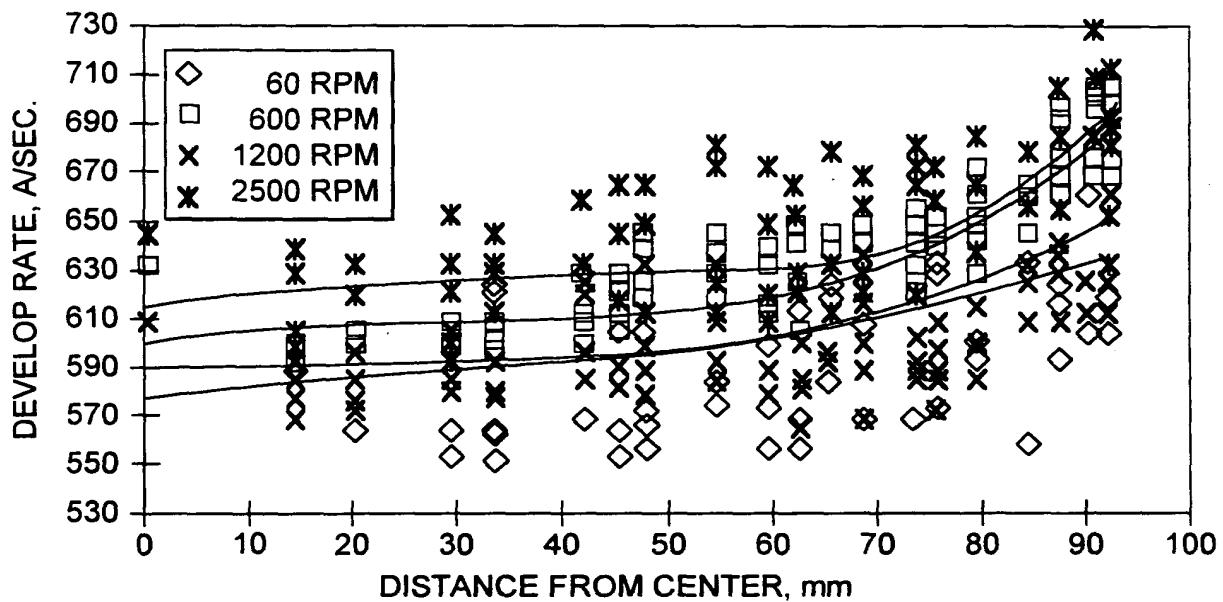


FIG. 15

DEVELOP RATES AT 121 WAFER LOCATIONS, DEVELOP ARM OFFSET=5mm
TOK 9; DOSE=12; DEVELOP TIME=10SEC.

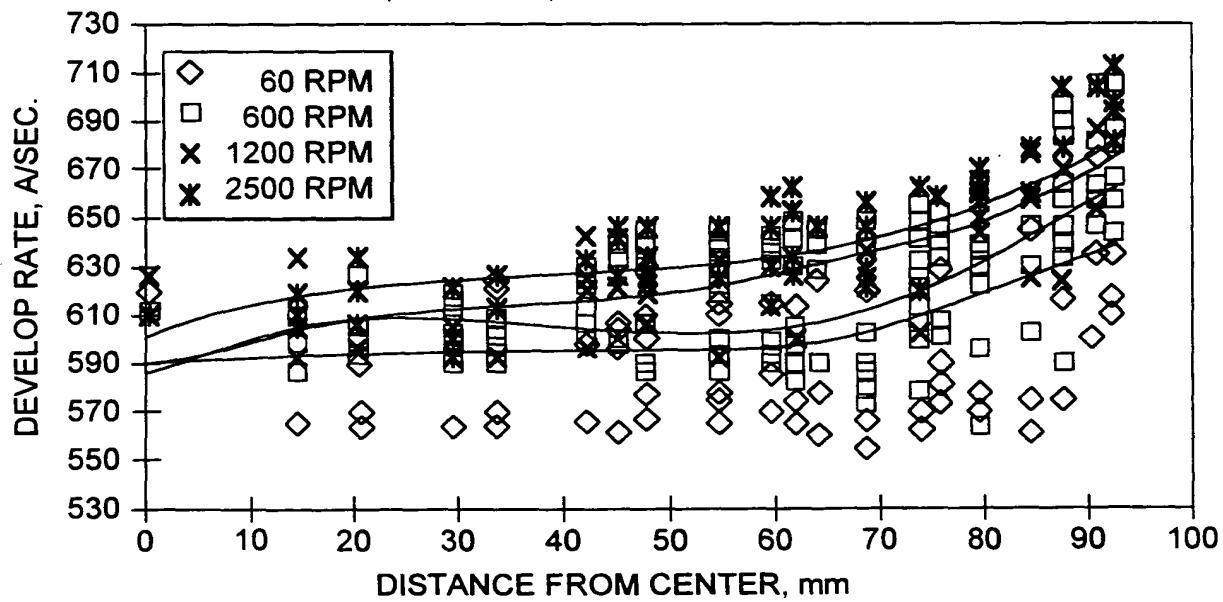


FIG. 16

DEVELOP RATES AT 121 WAFER LOCATIONS, DEVELOP ARM OFFSET=10mm
TOK 9; DOSE=12; DEVELOP TIME=10SEC.

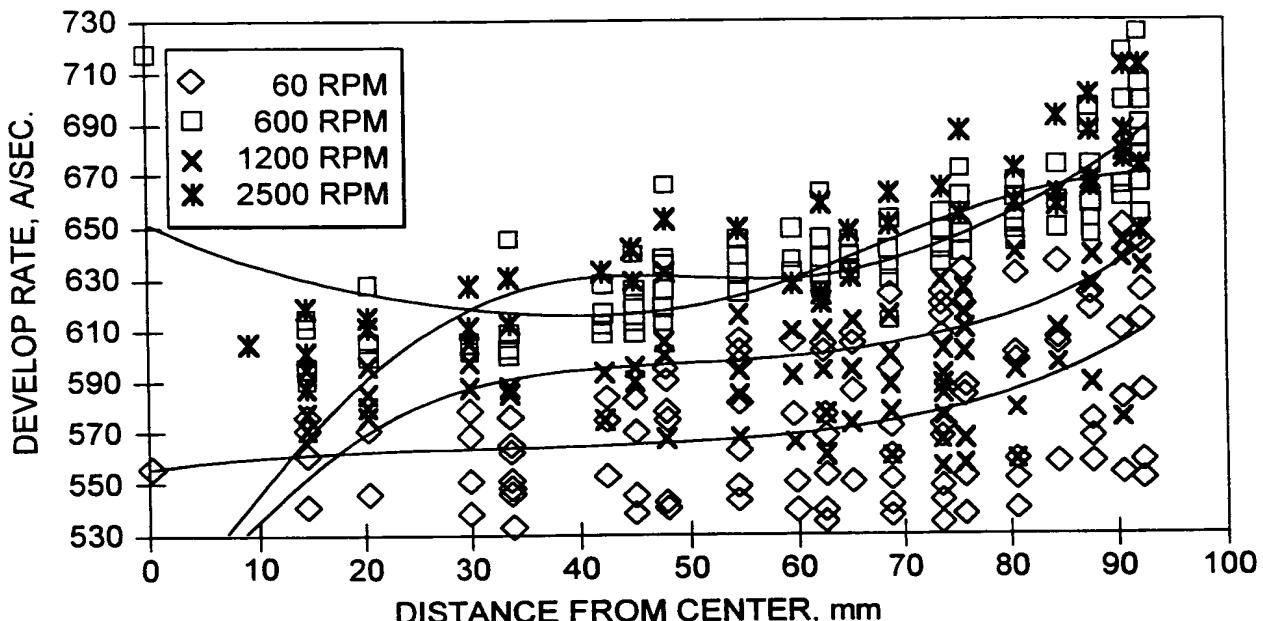


FIG. 17

DEVELOP RATES AT 121 WAFER LOCATIONS, DEVELOP ARM OFFSET=20mm
TOK 9; DOSE=12; DEVELOP TIME=10SEC.

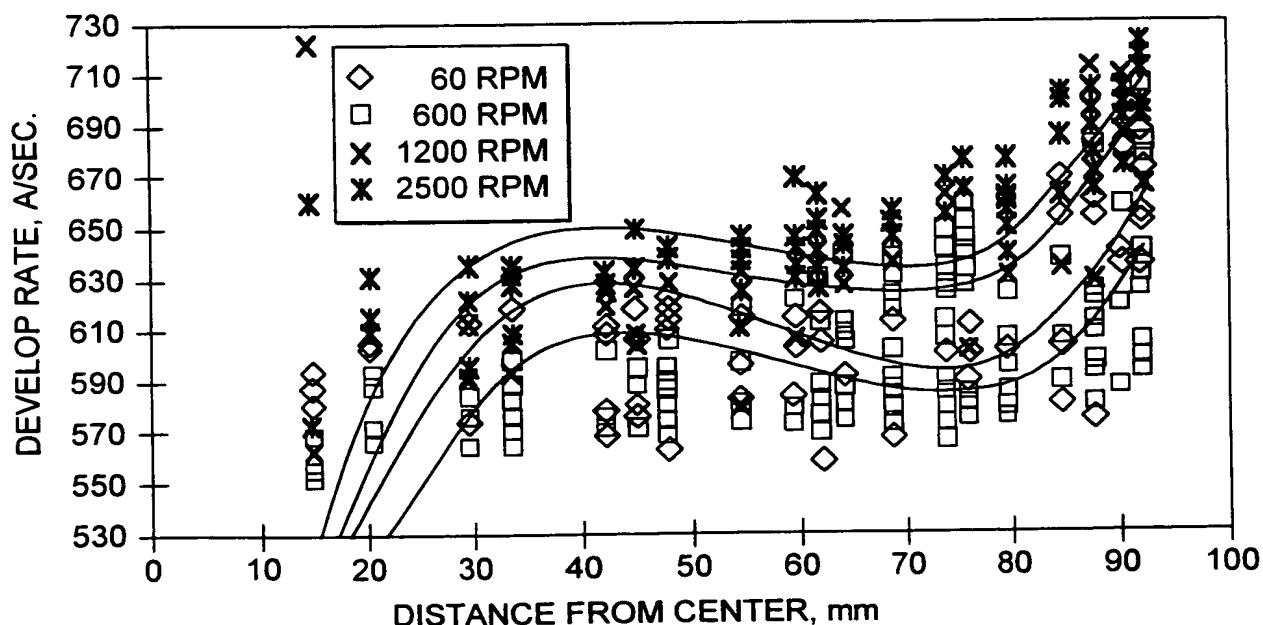


FIG. 18

FIG.19A

IPEC DEVELOP RATE MAPS FOR ARM OFFSET=0mm

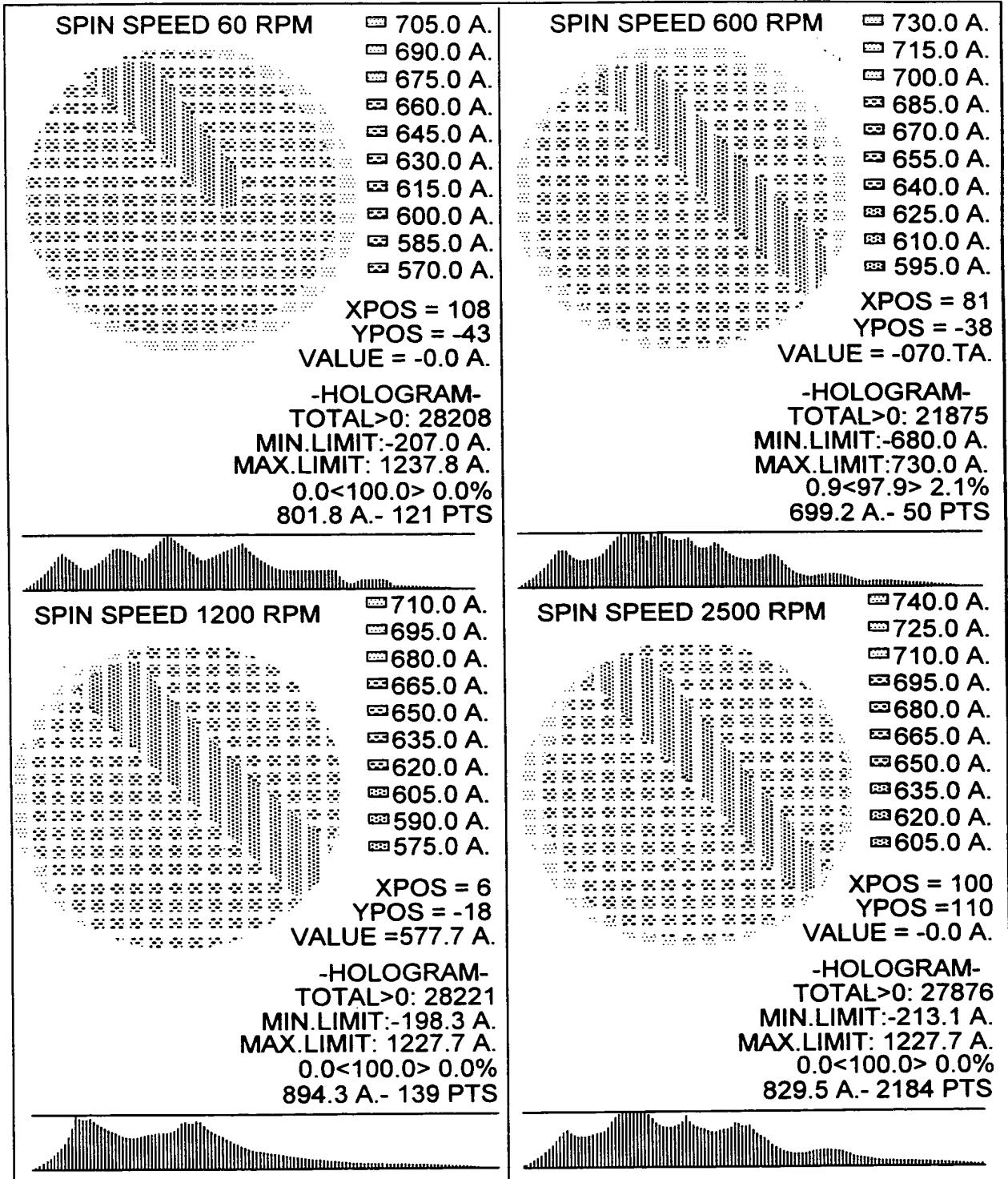


FIG.19C

FIG.19B

FIG.19D

FIG.20A

IPEC DEVELOP RATE MAPS FOR ARM OFFSET=5mm

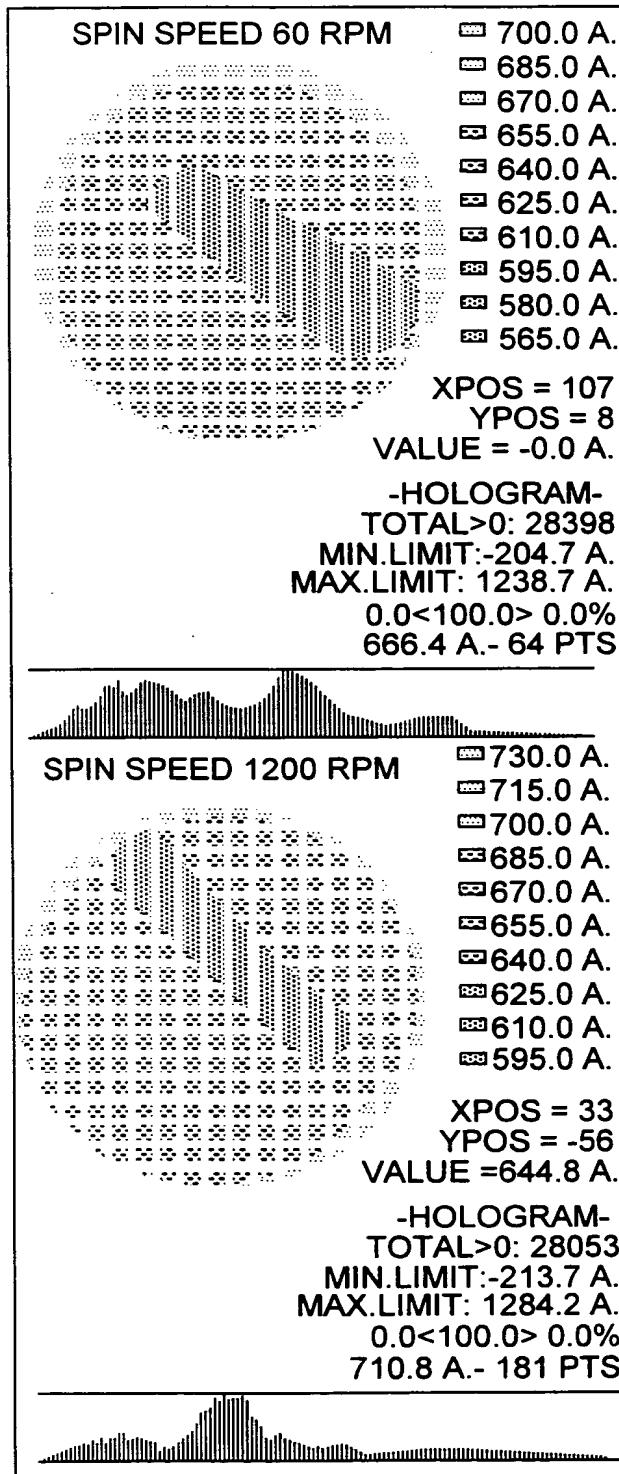


FIG.20C

FIG.20B

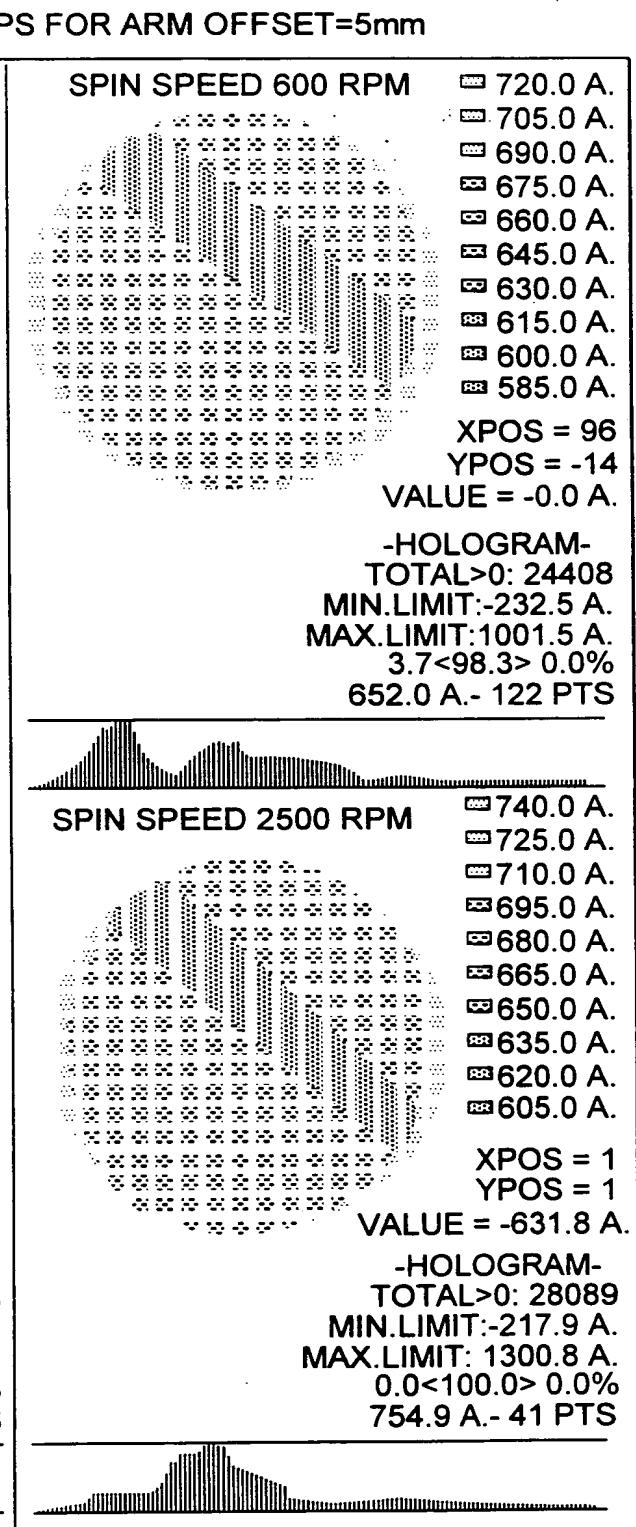


FIG.20D

FIG.21A

IPEC DEVELOP RATE MAPS FOR ARM OFFSET=10mm

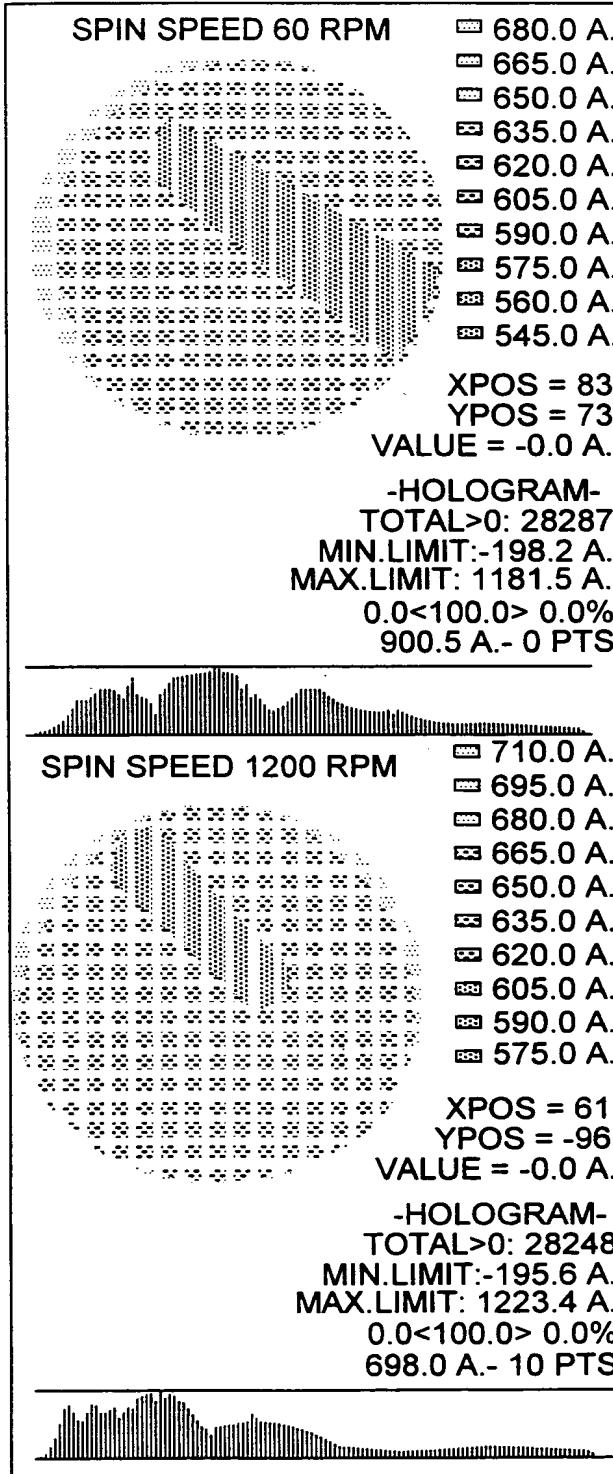


FIG.21C

FIG.21B

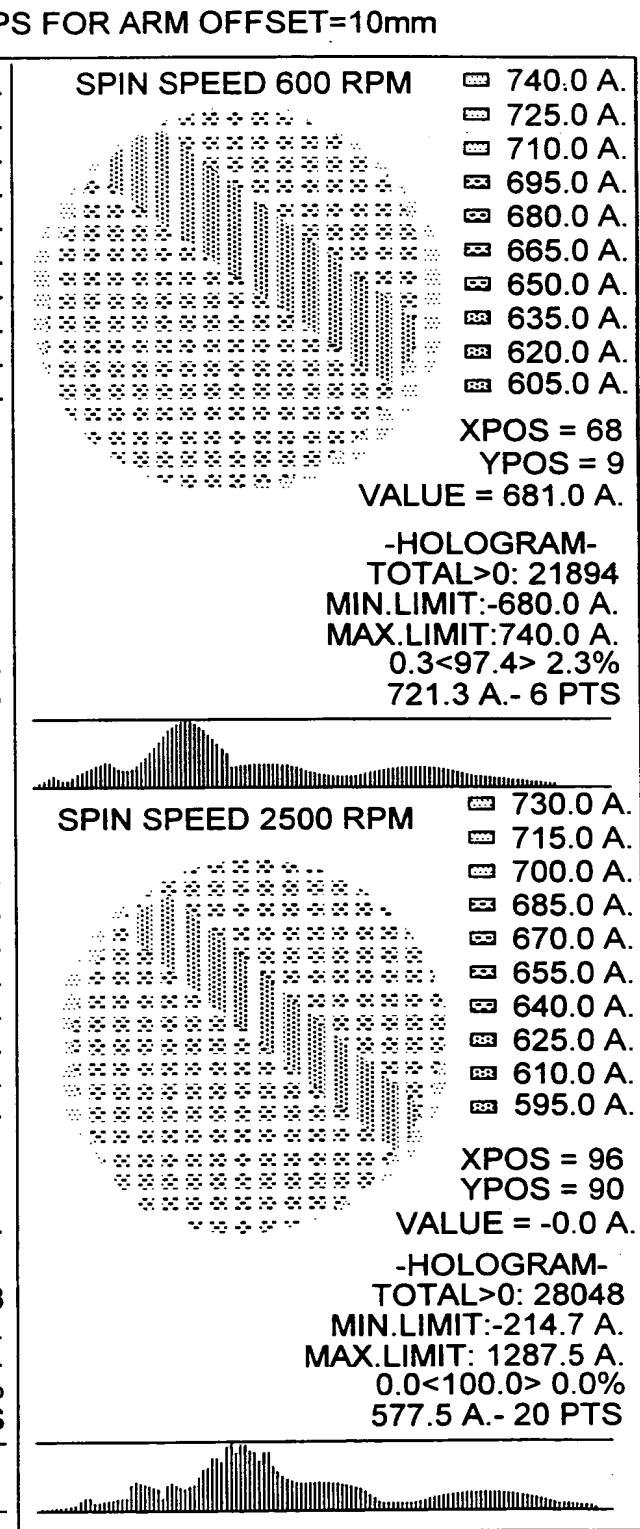


FIG.21D

FIG.22A

IPEC DEVELOP RATE MAPS FOR ARM OFFSET=20mm

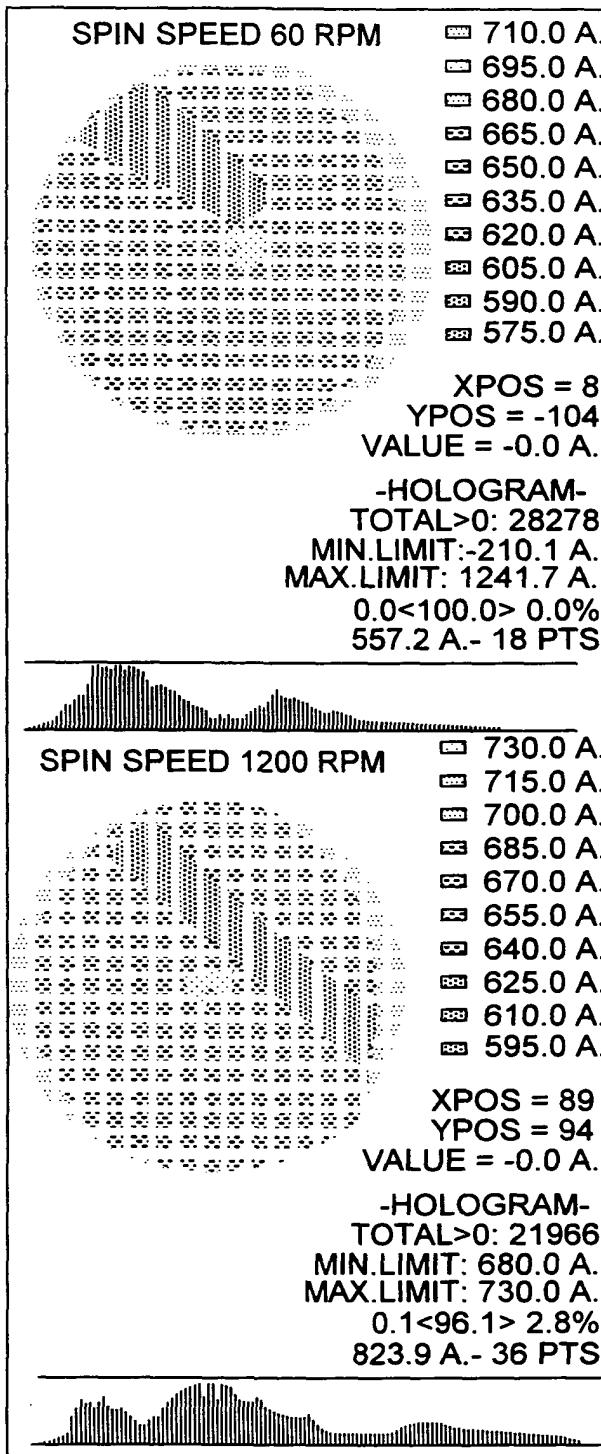


FIG.22C

FIG.22B

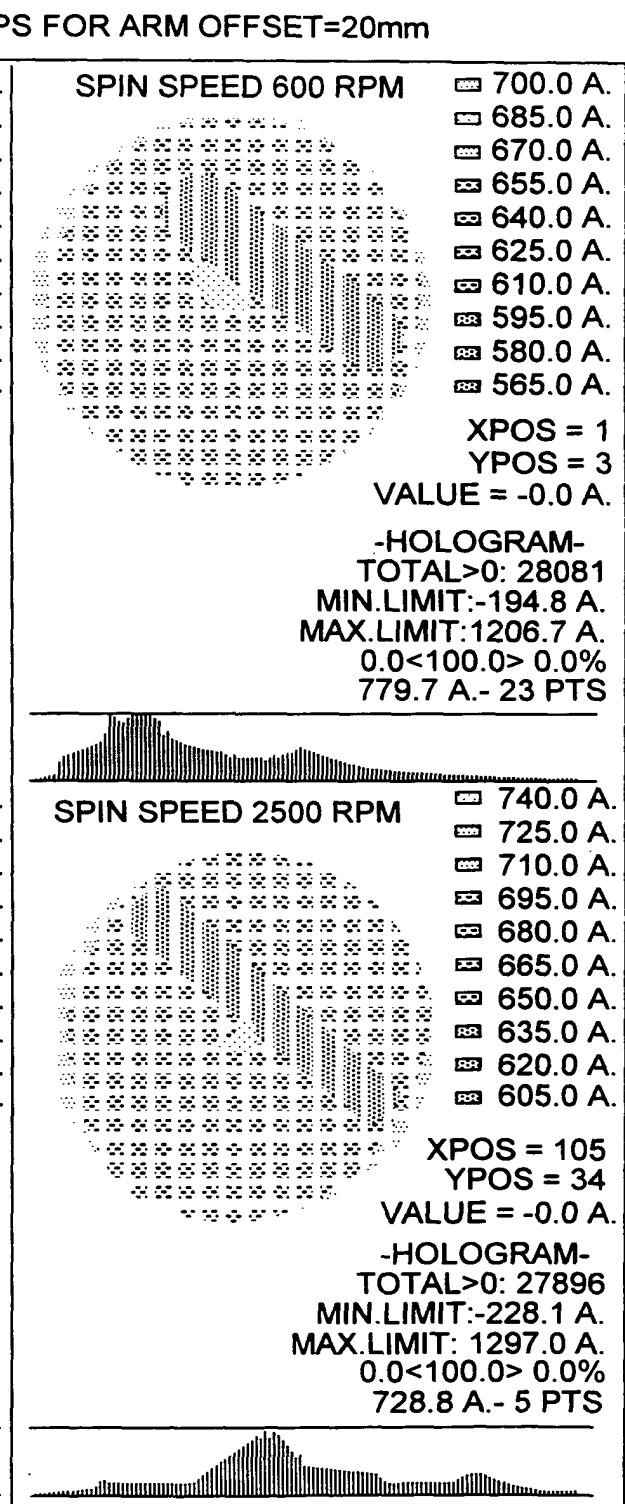


FIG.22D